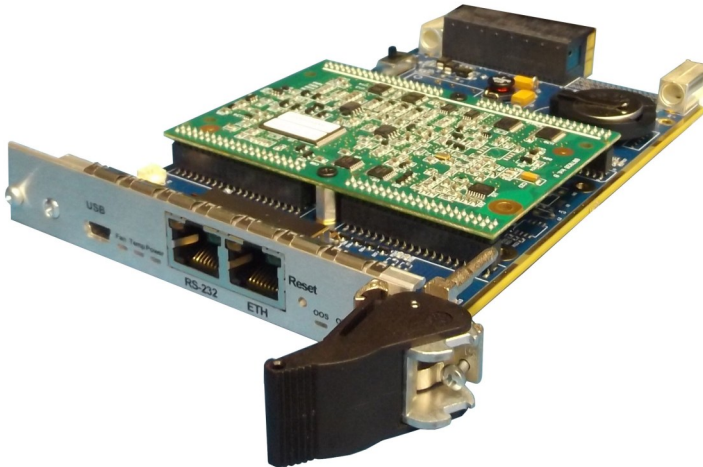




SHM200 KEY FEATURES

- Designed for OpenVPX systems and aligned to the SOSA® Technical Standard, Tier 3
- Compliant to VITA 46.11 for VPX System Management
- IPMI 2.0 compliant
- IPMI firewall supported
- I²C based real-time clock with battery backup
- Temperature monitoring of at least 6 temp sensors (consult factory for higher options)
- Fan monitoring of at least 10 fans (consult factory for higher options). Fan PWM/Tach control, 16 digital inputs and outputs
- Voltage monitoring of at least 6 voltages (consult factory for higher options)
- Sensor event log and data records
- USB, RS-232, and Ethernet interface options
- Rich suite of interface options including: telnet, SNMP, RMCP, CLI, etc.
- Low power consumption
- Customization available
- Conformal coating optional

Pixus offers OpenVPX Chassis Managers in standard designs as well as customized form factors. We utilize the Pixus SHM200 controllers as a basis for the technology. Customized front panel interfaces are also available.

The shelf manager monitors each of the power rails and provides an alarm function for voltage levels that fall below the defined acceptable range. The SHM200 can monitor cooling zones with thermal sensors located in the chassis. This allows fans to automatically speed up as needed to cool each zone properly. The single Ethernet port version is 10/100Base-T/TX or 100Base-KX or 1000Base-KX.

With a P0 VPX connector interface, the chassis manager can be plugged into any standard OpenVPX slot. There are other options that require additional connectors, which includes:

- P1 connector (with mechanical keying) for extended I/O. A 3-port Ethernet hub (Level 2 unmanaged) is also an option.
- P2 connector for Remote Reset/Maskable Reset. The option is available for boards in the system that need to be reset.

Conformal coating on the SHM200 OpenVPX Chassis Manager is optional. Pixus is ISO9001:2015 registered.

Specifications

Architecture		
Physical	Dimensions	Approx 3U tall x 160mm deep, 0.8" or 1.0" pitch depending on options
		6U form factors available (consult factory)
	Panel Interface	RS-232, RJ-45, & USB interface (air-cooled version)
	Connector Interface	VPX P0 connectors (standard), additional P1 and/or P2 connector optional
Standards		
VITA	Type	VITA 46.11, SOSA 2.0 Technical Standard
Configuration		
Power Usage		Approx. 4W Max.
Environmental	Temperature	Operating temperature: -40° to +70°C
		Storage temperature: -55° to +90°C
	PCB	FR-4 or equivalent
	PCB traces	0.5 oz. standard
Conformal coating		Upon request (See page 6 selection "J" for available options)
Other		
MTBF	MIL Handbook 217-F @ 344,022 Hrs.	
Certifications	Designed to meet FCC, CE and EN/UL/TUV certifications where applicable	
Warranty	Two years	
Trademarks and logos	The Pixus Logo is a registered trademark of Pixus Technologies Inc. other registered trademarks are the property of their respective owners. Specs. subject to change without notice.	



Ordering Options SHM200

SHM200-AB-CDE-FGJ-XX

A = ShMM Controller

- 0 = SHM200 (Pixus, standard)
- 1 = Reserved
- 2 = Other

B = Pitch

- 0 = 0.8" (standard for air cooled)
- 1 = 1.0" (standard for conduction cooled)
- 2 = Custom

C = Panel Height

- 0 = 3U
- 1 = 6U
- 2 = Custom

D = Panel Interface

- 0 = Air Cooled — RS-232, USB, RJ-45, & LEDs
- 1 = Conduction-cooled — USB & LEDs
- 2 = Custom

E = Ethernet Hub

- 0 = not installed
- 1 = 3-port Level 2 unmanaged (requires the P1 connector to be used)

F = Extended I/O

- 0 = P0 connector only (can plug into any standard OpenVPX backplane)
- 1 = P0 connector, P1 connector to access extended I/O (Tach, Ethernet, Digital I/O, temperature sensors, etc)*
- 2 = P0 connector, P1 for extended I/O, P2 for Remote Reset, GPIO*

* P1 and P2 connector options require backplane customization for those signals

G = Temperature Range

- 0 = Commercial (0 C to +55 C)
- 1 = Industrial/MIL (-20 C to +70 C)

J = Conformal Coating

- 0 = None
- 1 = Humiseal 1A33 Polyurethane
- 2 = Humiseal 1B31 Acrylic

2 digit customization code

Blank = standard, no customization